

L1: (19) encod\$4 near9 decod\$4 near9 multiplex\$4 near9 command

Failed

Saved

- S1: (1333) chip near9 pin near9 (expand\$4 extend\$4)
- S2: (4) (encod\$4 decod\$4) near9 S1
- S3: (1333) chip near9 pin near9 (transfer\$4 transmit\$4)
- S18: (363859) extend\$4 near5 pin
- S7: (208) decod\$4 near9 encod\$4 near9 pin
- S6: (835) chip near9 S5
- S5: (309181) extend\$3 near3 pin
- S8: (0) S6 with S7
- S9: (0) decod\$4 near9 encod\$4 near9 S6
- S4: (39) chip near5 pin near4 expand\$4
- S16: (32244) chip near9 pin
- S13: (8260) S11 near9 S12
- S12: (26010) second near2 chip
- S14: (11) S1 with S13
- S17: (70) S15 and S16
- S10: (13683) transfer\$3 near3 pin
- S20: (735) chip near5 S18
- S19: (7573) expand\$4 near3 pin
- S11: (33586) first near2 chip
- S15: (100) ("5822251" or ("4504959" or ("5555543" or ("5826093" or ("2005025
- S21: (3866) (encod\$4) near9 (decod\$4) near9 (command instruction)
- S23: (33) chip near9 S21
- S22: (1) (chip near5 pin) near9 S21
- S24: (3) command near9 encoder near9 decoder near9 chip.
- S25: (1) ("20060149859".PN.
- S27: (8) command near9 S26
- S26: (1530) (expan\$4 exten\$4) near9 pin near9 chip
- S29: (28) command near9 S28
- S30: (18) interface near9 S28
- S28: (740) transfer\$4 near9 pin near9 chip

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DBs US-PGPUB:USPAT:USOCR:FPBS:EPO:JPO:DERWENT:IBM\_TDB

Plurals

Highlight all hit terms initially

Default operator: OR

BRS form IS&R form Image Text HTML

	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	R	Inventor	S	C	P	Image Doc. Dis
1	<input checked="" type="checkbox"/>	US 20070019743	20070125	19	Video compression encoder	375/240.26			Dambrackas; William A. e	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20070019743
2	<input checked="" type="checkbox"/>	US 4958277 A	19900918	18	Queued serial peripheral interfa	710/52	710/62		Hill; Susan C. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4958277
3	<input checked="" type="checkbox"/>	US 6331784 B	20011218	5	Secure programmable logic inte				KUNNARI, N D et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6331784
4	<input checked="" type="checkbox"/>	US 4710927 A	19871201	10	Diagnostic circuit	714/724	714/731		Miller; Michael J.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 4710927
5	<input checked="" type="checkbox"/>	US 20050262289	20051124	33	Semiconductor integrated circu	711/100			Okuda; Yuichi	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 20050262289

Hits Details HTML

Ready

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